Code: xilinx_xcku5p
Comcode: 8600300853P

CROSS SECTION DETAIL (Top to Bottom)

Layer	Est. Start Material	Finish Thickness (Nom)	Units
	LPI_SMOBC	1.50	mil
01	COPPER(1_OZ)OUTER	2.50	mil
	BSTAGE/DIELECTRIC	2.90	mil
02	COPPER(1/2_OZ)INNER	0.60	mil
	FR4_CORE	4.50	mil
03	COPPER(1/2_OZ)INNER	0.60	mil
	BSTAGE/DIELECTRIC	4.50	mil
04	COPPER(1/2_OZ)INNER	0.60	mil
	FR4_CORE	4.50	mil
05	COPPER(1/2_OZ)INNER	0.60	mil
	BSTAGE/DIELECTRIC	4.50	mil
06	COPPER(1_OZ)INNER	1.20	mil
	FR4_CORE	8.00	mil
07	COPPER(1_OZ)INNER	1.20	mil
	BSTAGE/DIELECTRIC	8.00	mil
08	COPPER(1_OZ)INNER	1.20	mil
	FR4_CORE	8.00	mil
09	COPPER(1_OZ)INNER	1.20	mil
	BSTAGE/DIELECTRIC	8.00	mil
10	COPPER(1_OZ)INNER	1.20	mil
	FR4_CORE	8.00	mil
11	COPPER(1_OZ)INNER	1.20	mil
	BSTAGE/DIELECTRIC	4.50	mil
12	COPPER(1/2_OZ)INNER	0.60	mil
	FR4_CORE	4.50	mil
13	COPPER(1/2_OZ)INNER	0.60	mil
	BSTAGE/DIELECTRIC	4.50	mil
14	COPPER(1/2_OZ)INNER	0.60	mil
	FR4_CORE	4.50	mil
15	COPPER(1/2_OZ)INNER	0.60	mil
	BSTAGE/DIELECTRIC	2.90	mil
16	COPPER(1_OZ)OUTER	2.50	mil
	LPI_SMOBC	1.50	mil
Total Board Thickness		101.8	

History Detail
Nov 08 14:46:17 2019
Initial Release

AM1: New Design.

SUMMARY DETAILS

Product Name: xilinx_xcku5p
ProjectName: xilinx_xcku5p
Comcode/Artmaster/Issue: 8600300853P/am1 Issue 1
Panel Specification: no panel
Circuit Dimension: X 6300.00 Y 6200.00 Units mils
Number of Layers: 16
Number of Through Holes per circuit: 136
Number of SMT pads per circuit: 386
Total number of drill holes in circuit: 658
Smallest Via [finshed hole size]: 0.015
Overall Nominal PWB Thickness: 101.8
Starting CU Weight (By Layer): 1/0.500/0.500/0.500/0.500/1/1/1/1/1/1/0.500/0.500/0.500/0.500/1
Found Core Thickness: 8.00
Found Core Thickness: 2.90
Found Core Thickness: 4.50

Power Systems
Proprietary
Use Pursuant to
Company Instructions

NAME: xilinx_xcku5p_1
COMCODE: 8600300853P
PWB PROJECT: xilinx_xcku5p_1
UNITS: "mils"
CKT X-DIM: 6300.0
CKT Y-DIM: 6200.0
LAYERS: 16

TOTAL SMT Pins: 386
TOTAL TH Pins: 136
TOTAL Drill Holes: 658
Overal Nom PWB Thickness: 101.8
Panel: no panel
Previous PWB Comcode: INITIAL RELEASE
Product Category: adw_projects

Date: 110819
User: 204063196

TITLE/ID	COMCODE:	ISSUE:
8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	8600300853P	1
DESCRIPTION/VIEW	PAGE COUNT: / TYPE:	SBU:
PRINTED WIRING BOARD Fabrication Notes	1 OF 27 / PWB	AM1

1. Unless otherwise specified, dimensions on this drawing and gerber data are expressed in mils. Tolerances on this drawing and gerber data when converted to inches are:
   X.XX +/- 0.01
   X.XXX +/- 0.005
Board Cross Section Requirements:
   Outer Layer Copper Thickness: +20% / -10%
   Inner Layer Copper Thickness: +/- 10%
   Overall Board Thickness: +/- 10%
   Absolute Minimum dielectric spacing shall be 0.00354 in. for 1500V (see Dielectric strength Note 4).
   Dielectric Spacings: +/- 10%
   Angles +/- 2 Degrees
Board Feature Requirements:
   Unspecified radii: 0.047
   Hole to hole: +/- 0.003
   PTH diameter: +/- 0.003
   NPTH diameter: +/- 0.002

2. WORKMANSHIP SPECIFICATION: Class III per latest issue of IPC-6012, IPC-600, and IPC-6011 latest issue, unless otherwise specified. Annular ring to be 1 mil minimum. Via pads may be tear-dropped to meet the annular ring requirement. Soldermask shall meet IPC-600 Class II thickness requirements.

3. Cross section detail specifies expected nominal thicknesses in mils for materials used in board stackup. Two sheet minimum B-stage/dielectric. Nominal copper thicknesses by weight derived from IPC-4562. Minimum 0.0010 in. copper plating required in all plated through holes. Outer layer copper thicknesses defined in board cross section detail includes nominal surface copper plating thickness of 0.0012 in. Overall board thickness is measured from outside of soldermask top to outside of soldermask bottom.

4. Dielectric strength shall be capable of withstanding 1500V +25/-0V DC for 60 +5/-0 seconds. Absolute minimum dielectric spacing specified in tolerance table MUST be met to secure this rating.

5. FINISH: Lead Free Hot Air Solder Level (HASL) per IPC 6012A or latest issue. Solder is Sn/0.7Cu/0.05Ni. Thickness can vary between 100 micro-inches to 1500 micro-inches. For consistent measurement correlation, minimum thickness shall be measured in the center of the largest visible pad on both top and bottom of finished board.

6. Apply soldermask over bare copper (SMOBC). Pb-free compatible and green color. Only LPI soldermasks must be used and comply with IPC-SM-840 Class T or latest issue. Approved soldermasks are TAIYO PSR-4000-G23K, TAIYO PSR-4000-Z26, and Enthone DSR-3241(G). Soldermask to be 0.4 mils minimum at the knee of copper traces and a maximum of 2.0 mils on copper plateau. No mask allowed inside or covering through holes, except filled vias. If applicable, vendor required to audit vendor produced soldermask artwork versus GE supplied artwork and request written approval for changes.

7. If applicable, silkscreen using a permanent non-conductive white epoxy ink; registration to be within +/-0.020 in. Characters are to be legible to unaided eye, see IPC-A-600D.

8. This PWB must have a flammability rating of UL 94V-0, and UL CMOT of 130C or greater. The finished PWB must be UL recognized and have the manufacturer UL identification code and date code marked on the PWB. All vendor markings must be contained within the specified areas defined on design artwork layer or dimensioned detail print within drawing as indicated by the nomenclature:
  ✱VM✱
Where "✱VM✱" represents the allowable area for placement of the manufacturer's UL identification code and a date code having the syntax "WW-YY" representing WEEK-YEAR. Metalization may NOT be changed without written GE approval.

9. Manufacturer to mark each individual circuit in the panel with unique alpha character[s]. Character to be placed in the vendor marking area, unless otherwise indicated on fabrication drawing. If PWB is not panelized, no individual circuit marking is required.

10. Electrically test all product for continuity. Rejected [X-out] boards are acceptable in 4% of the lot to be shipped. No more than 25% of any one panel can be X-out. Mark rejects clearly on both sides, preferably with white markings. All X-out product to be gathered together, packaged in separate groups by X-out location, and shipped in a box that is clearly marked "X-out Product".

11. Certificate of compliance per latest MPS180000 to be provided with each lot including measurements of all specified parameters which have tolerances. PWBs may be rejected if supplier's Certificate of Compliance is missing.

12. Design, materials, and process changes require documented approval from both GE purchasing and GE engineering.

13. No Panel.

Power Systems
Proprietary
Use Pursuant to
Company Instructions

NAME: xilinx_xcku5p_1
COMCODE: 8600300853P
PWB PROJECT: xilinx_xcku5p_1
UNITS: "mils"
CKT X-DIM: 6300.0
CKT Y-DIM: 6200.0
LAYERS: 16

TOTAL SMT Pins: 386
TOTAL TH Pins: 136
TOTAL Drill Holes: 658
Overal Nom PWB Thickness: 101.8
Panel: no panel
Previous PWB Comcode: INITIAL RELEASE
Product Category: adw_projects

Date: 110819
User: 204063196

TITLE/ID	COMCODE:	ISSUE:
8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	8600300853P	1
DESCRIPTION/VIEW	PAGE COUNT: / TYPE:	SBU:
PRINTED WIRING BOARD Fabrication Notes	2 OF 27 / PWB	AM1

14. There shall be no feature-to-feature spacing less than what is shown in the table below, even if the features are electrically common [i.e., on the same net]. Any clearance present in the gerber data must be present in the fabricated board, subject to workmanship tolerances for the requested class of construction.

Starting copper weight (ounces)	Minimum design spacing line-line/pad-pad
0.5	5/5
1	7/7
2	9/9
3	12/12
5	15/17

15. MATERIAL: FR-4 Grade, High Tg, Non-Dicy [FILLED Phenolic], 1000hr Anti-CAF: PCL FR-370HR, TU-752, TU-768, Iteq IT-180A, and EMC-827. Non-phenolic or Dicy material is not allowed.
Once a design is qualified, no changes to material or construction can be made without GE approval.
All other materials must be approved in advance by GE.
Once a design is qualified, no changes to material or construction can be made without GE approval.

16. Packaging: Enclose a maximum of 10 panels in individually vacuum-sealed packages.

Power Systems
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COMCODE: 8600300853P
PWB PROJECT: xilinx_xcku5p_1
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Date: 110819
User: 204063196

TITLE/ID
8600300853P PRINTED WIRING BOARD AM1
8600300853P-PWB

COMCODE: 8600300853P

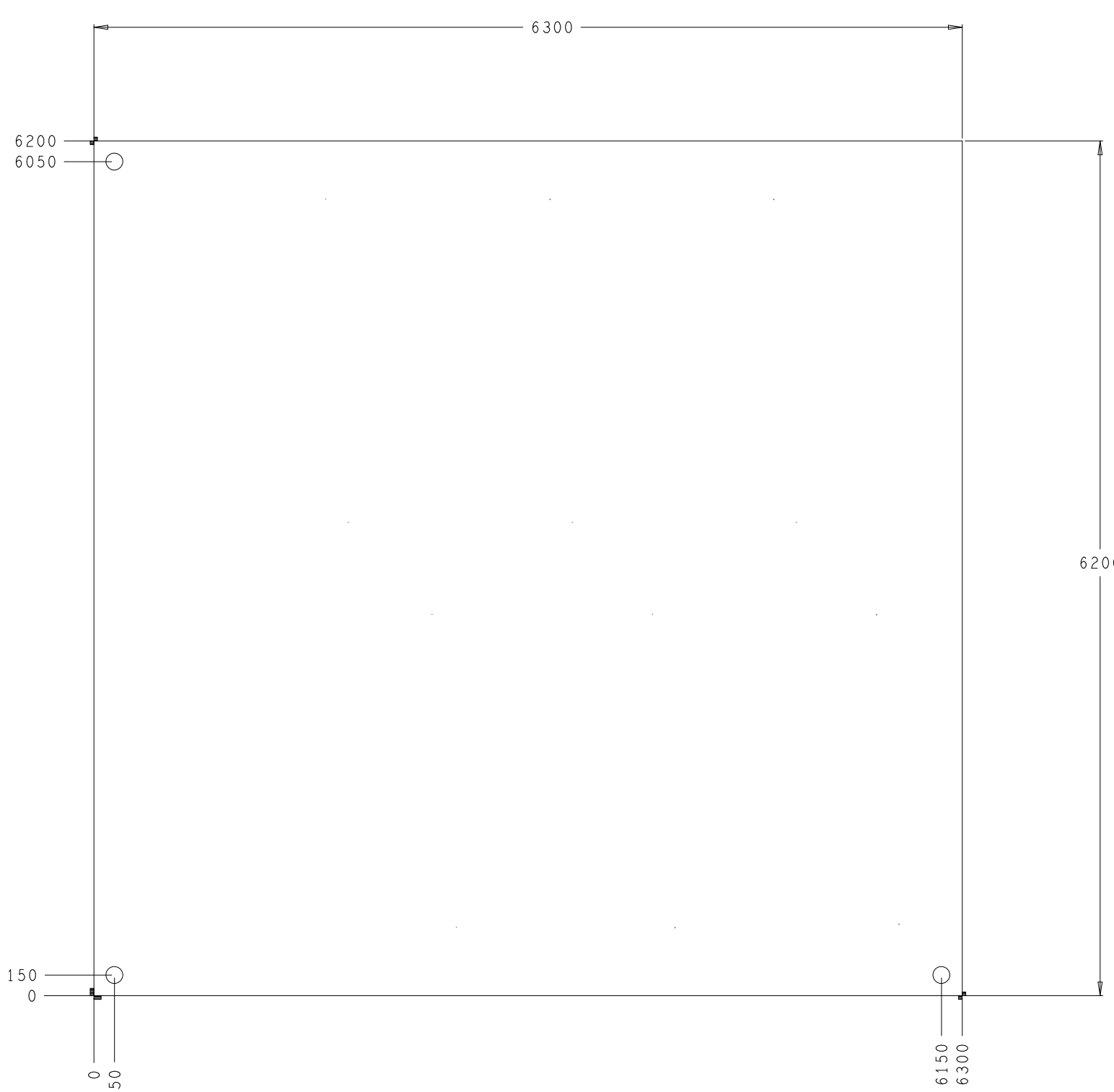
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DESCRIPTION/VIEW
PRINTED WIRING BOARD Fabrication Notes

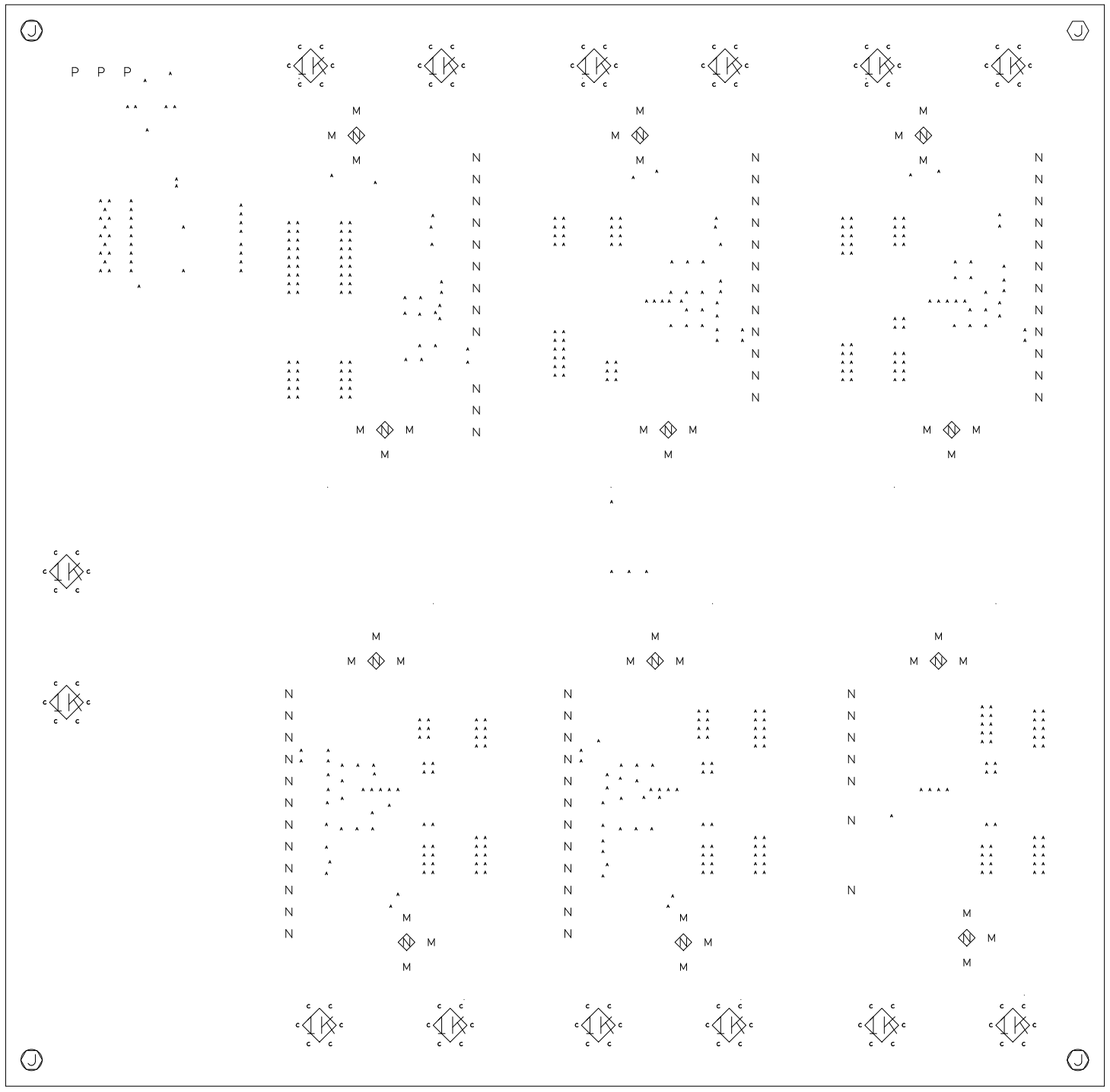
PAGE COUNT: 3 OF 27

TYPE: PWB

SBU: AM1



Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE/ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Board Outline/Route Detail	PAGE COUNT: 4 OF 27	TYPE: PWB	SBU: AM1



DRILL CHART: TOP to BOTTOM					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	TOLERANCE	PLATED	QTY	DRILL SIZE
A	15.0		PLATED	438	
C	22.0		PLATED	84	
M	41.0		PLATED	36	
N	43.0		PLATED	67	
P	51.0		PLATED	3	
◇	95.0		PLATED	12	
◇	197.0	+5.0/-1.0	PLATED	14	
Ⓙ	125.0		NON-PLATED	4	

Power Systems
Proprietary
Use Pursuant to
Company Instructions

NAME: xilinx_xcku5p_1
COMCODE: 8600300853P
PWB PROJECT: xilinx_xcku5p_1
UNITS: "mils"
CKT X-DIM: 6300.0
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LAYERS: 16

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Previous PWB Comcode: INITIAL RELEASE
Product Category: adw_projects

Date: 110819
User: 204063196

TITLE/ID
8600300853P PRINTED WIRING BOARD AM1
8600300853P-PWB

COMCODE: 8600300853P

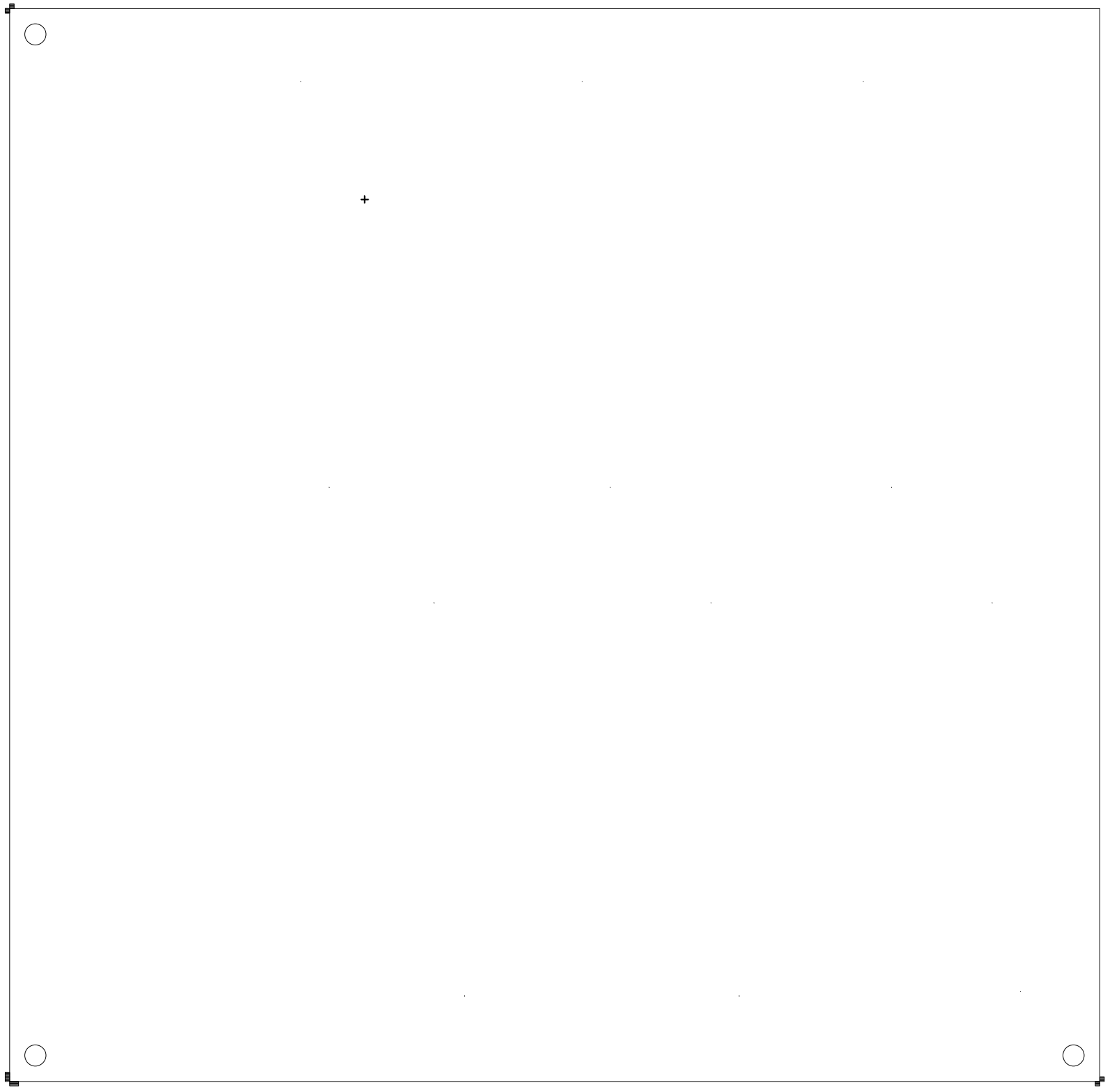
ISSUE: 1

DESCRIPTION/VIEW
PRINTED WIRING BOARD Drill Locations 1-16 [Top View]

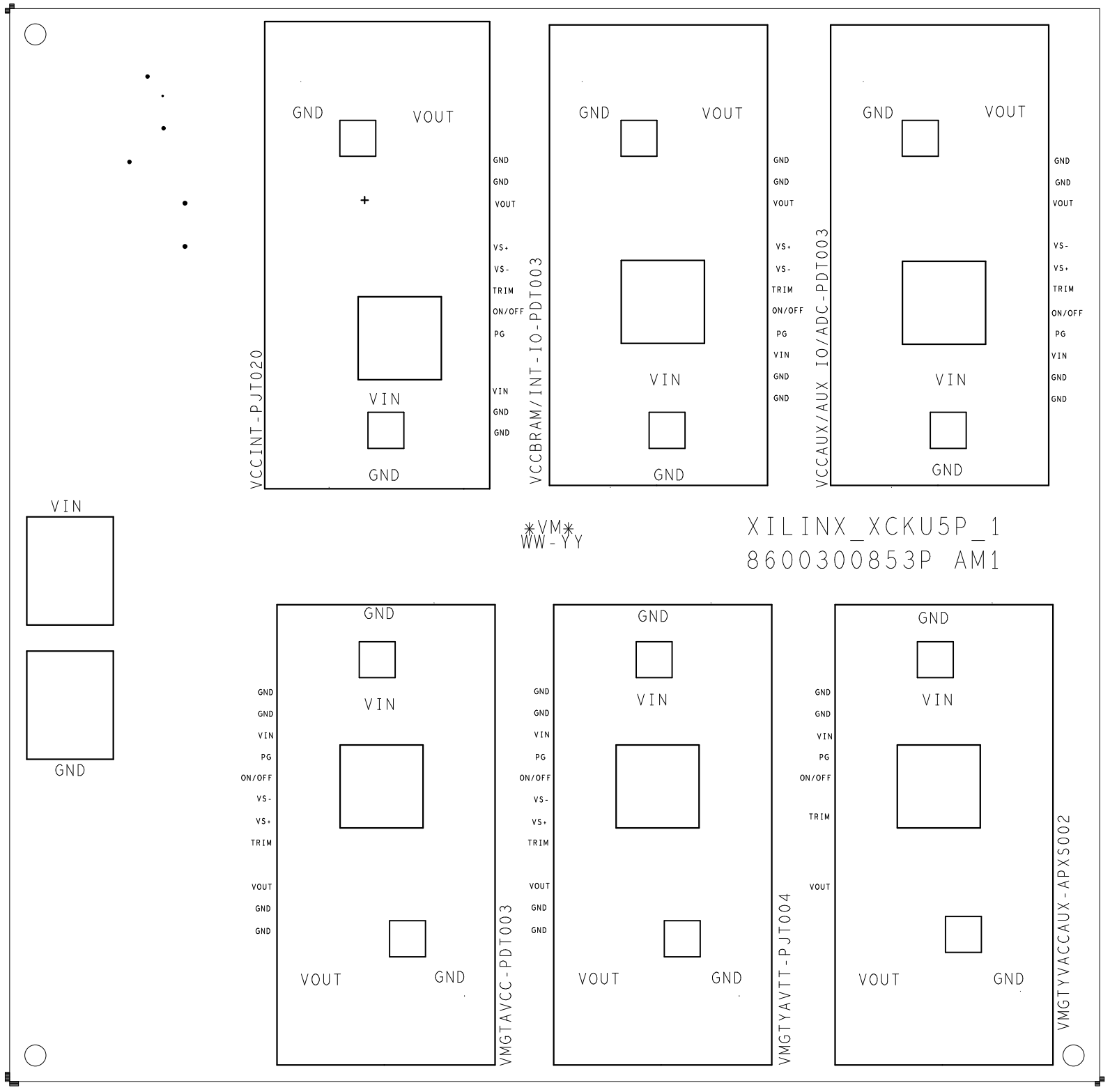
PAGE COUNT: 5 OF 27

TYPE: PWB

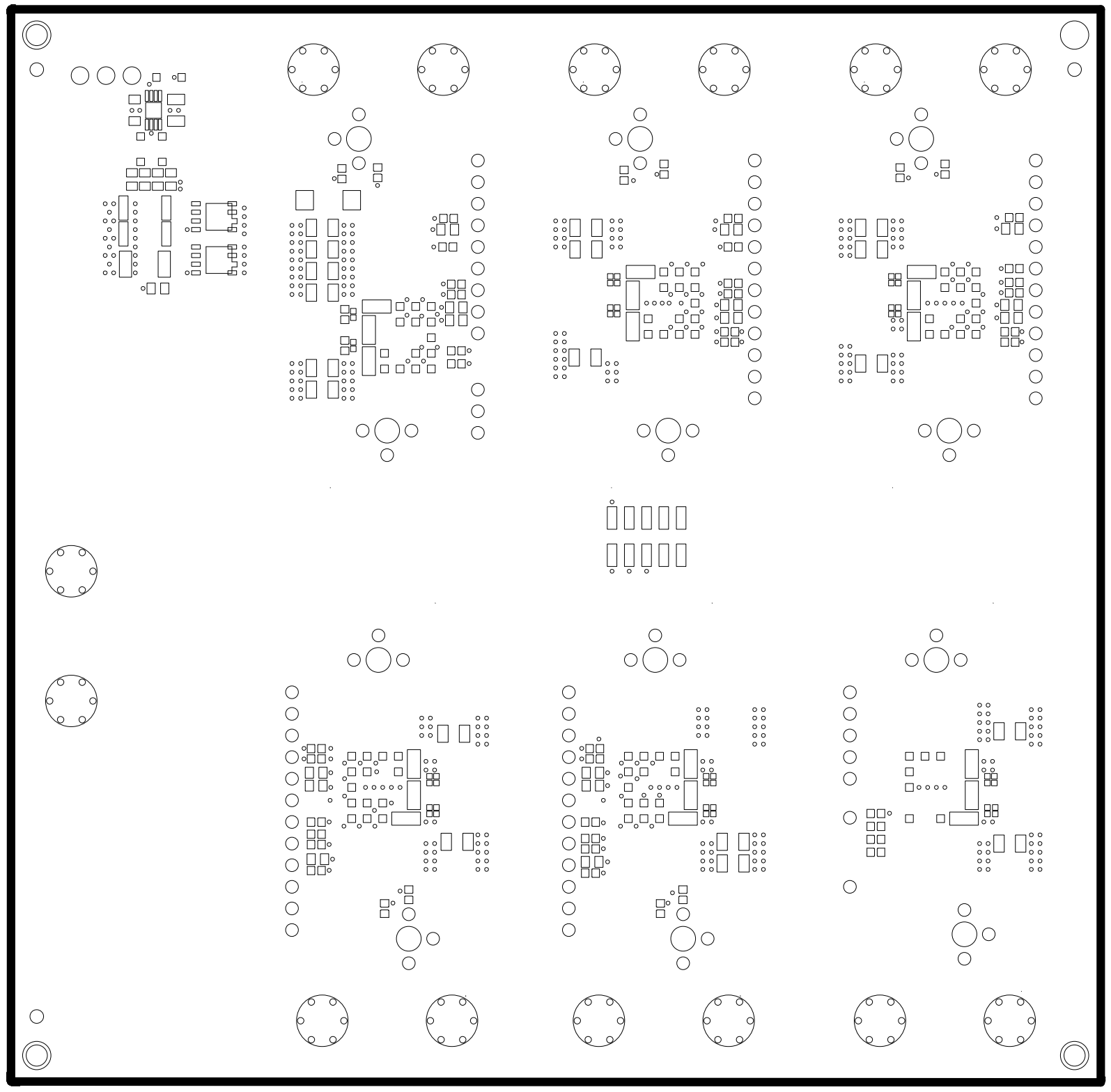
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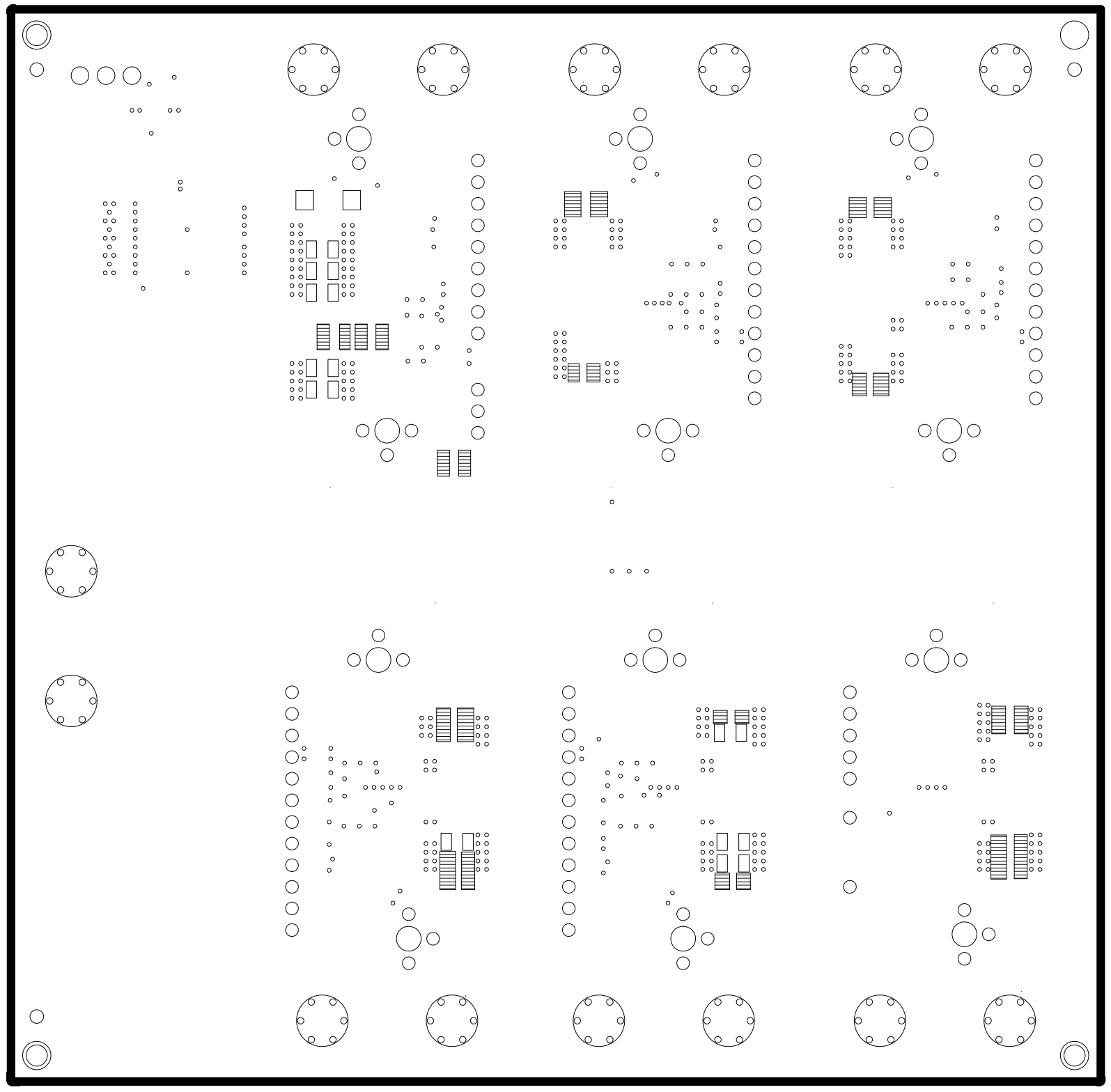
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Bottom Silk Screen [Top View]	PAGE COUNT: 6 OF 27	TYPE: PWB	SBU: AM1



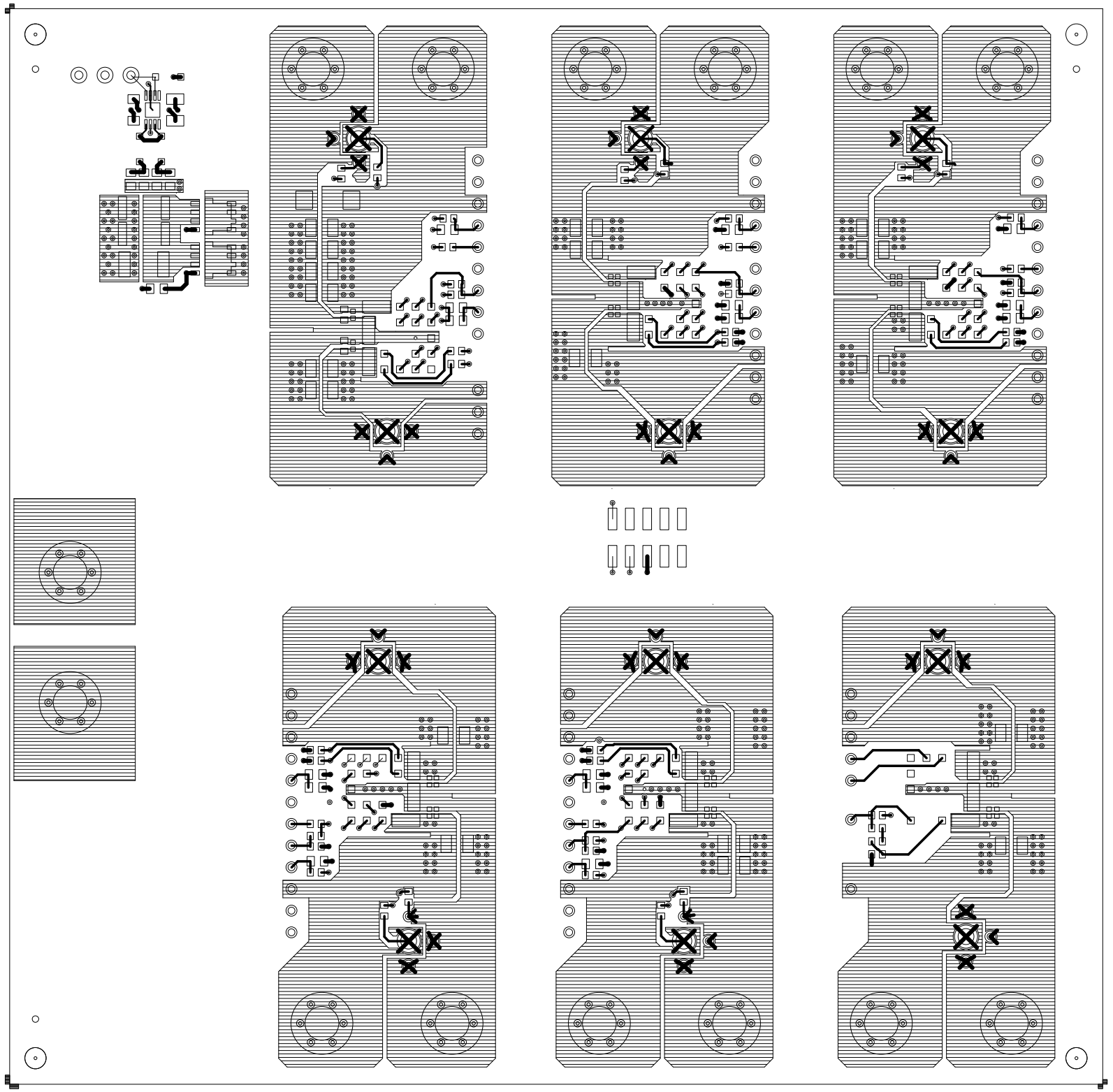
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Top Silk Screen [Top View]	PAGE COUNT: 7 OF 27	TYPE: PWB	SBU: AM1



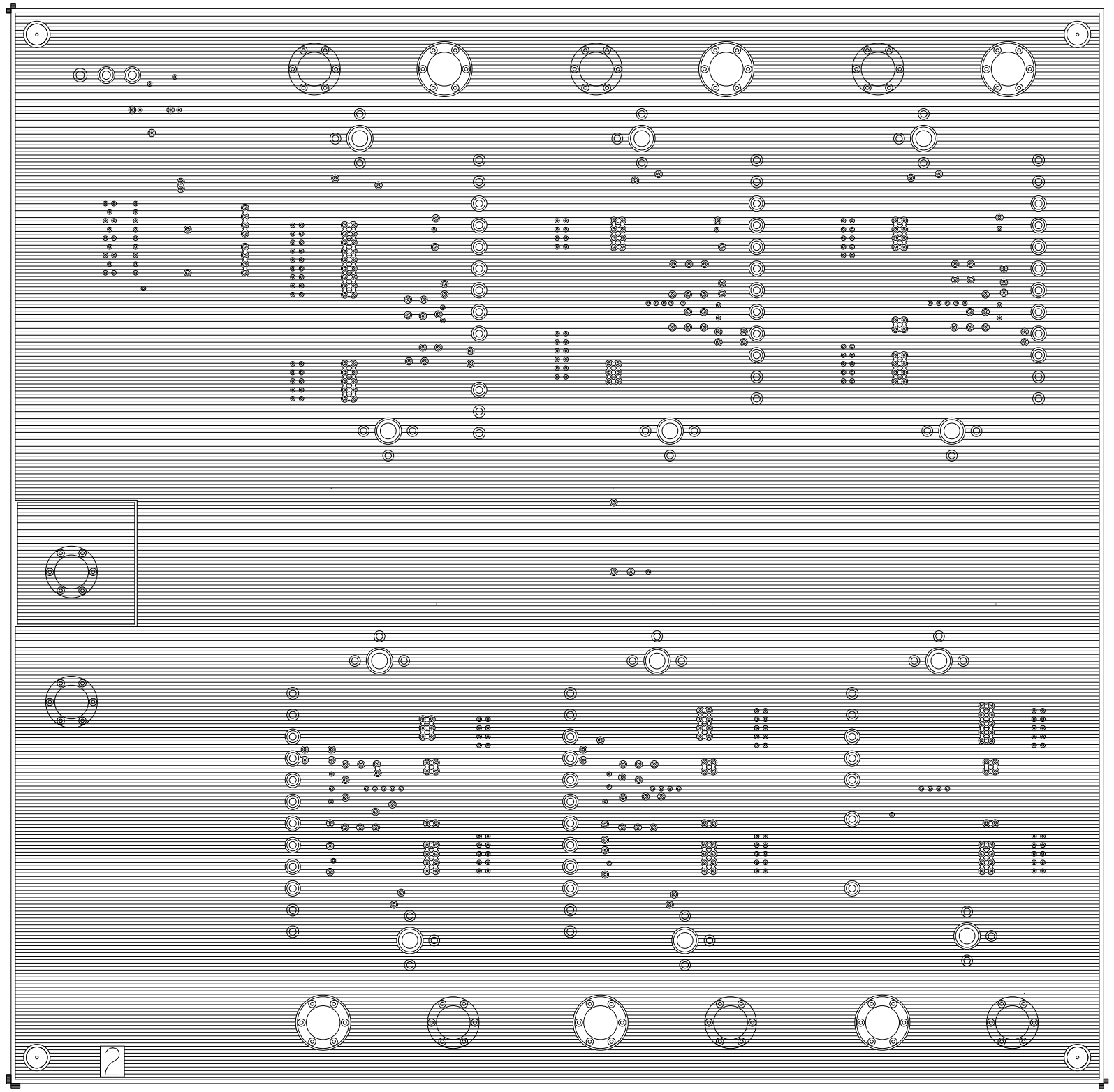
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Top Solder Mask [Top View]	PAGE COUNT: 8 OF 27	TYPE: PWB	SBU: AM1



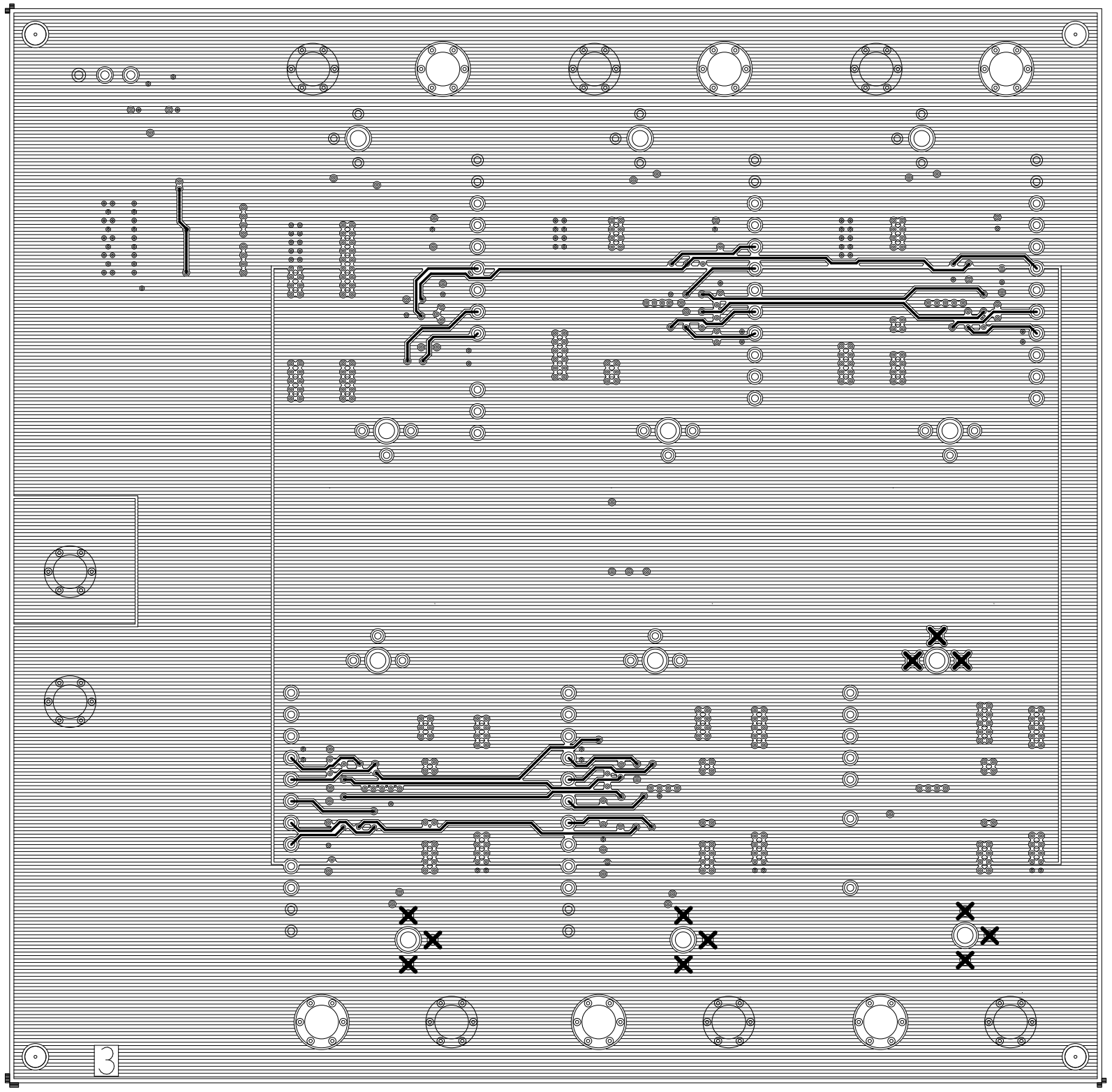
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Bottom Solder Mask [Top View]	PAGE COUNT: 9 OF 27	TYPE: PWB	SBU: AM1



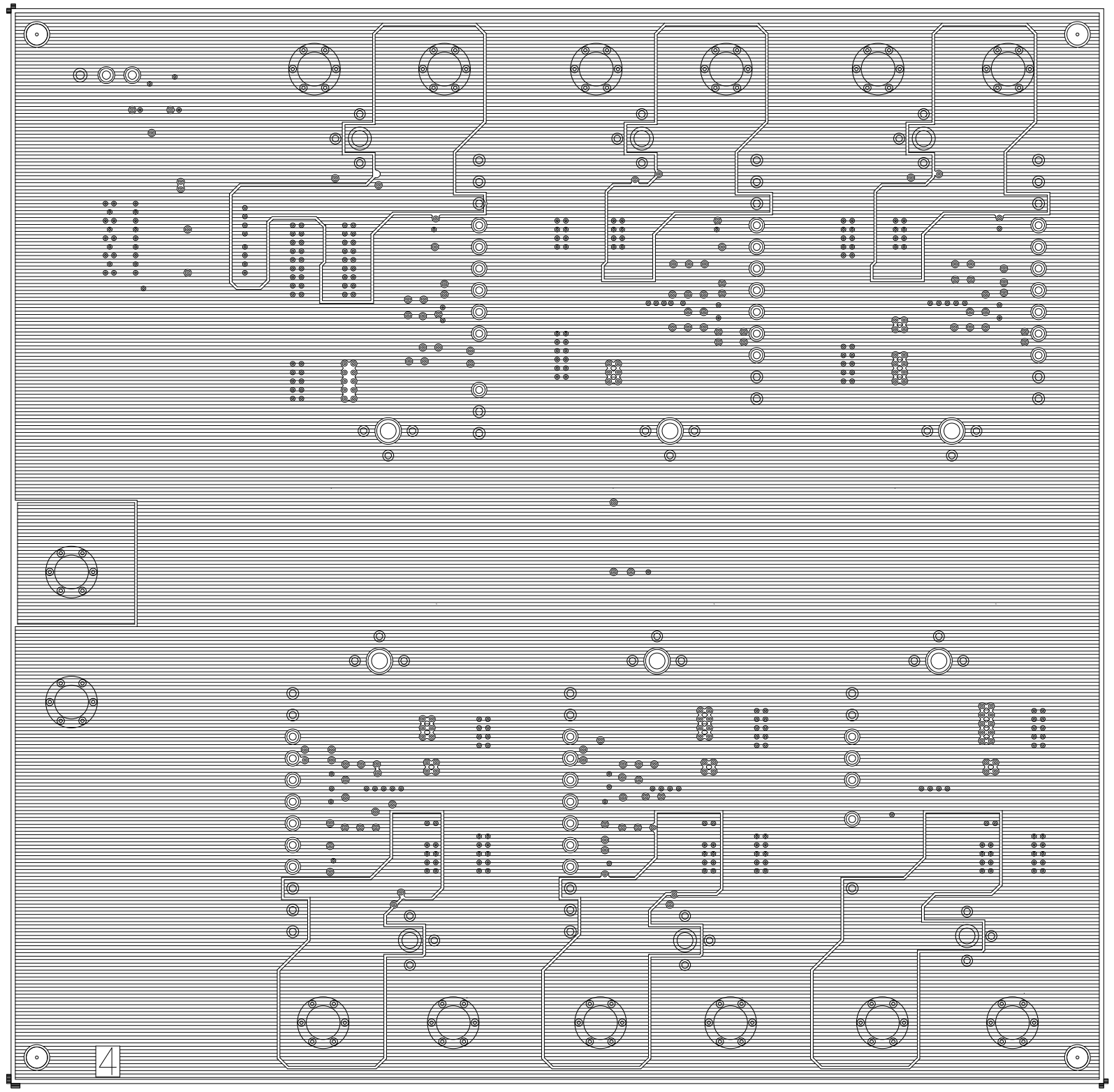
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 1 [TOP] Etch [Top View]	PAGE COUNT: 10 OF 27	TYPE: PWB	SBU: AM1



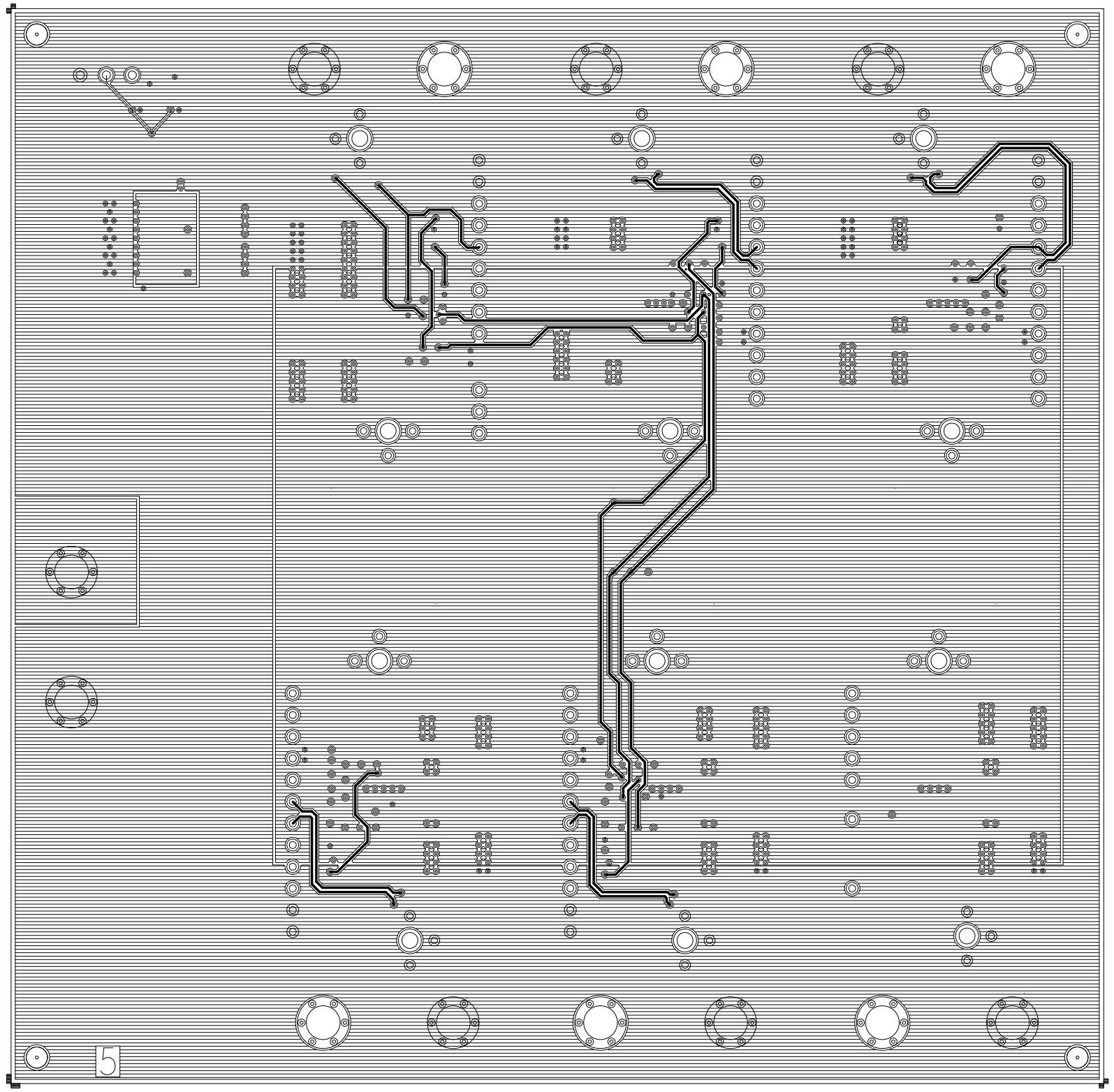
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 2 Foil [Top View]	PAGE COUNT: 11 OF 27	TYPE: PWB	SBU: AM1



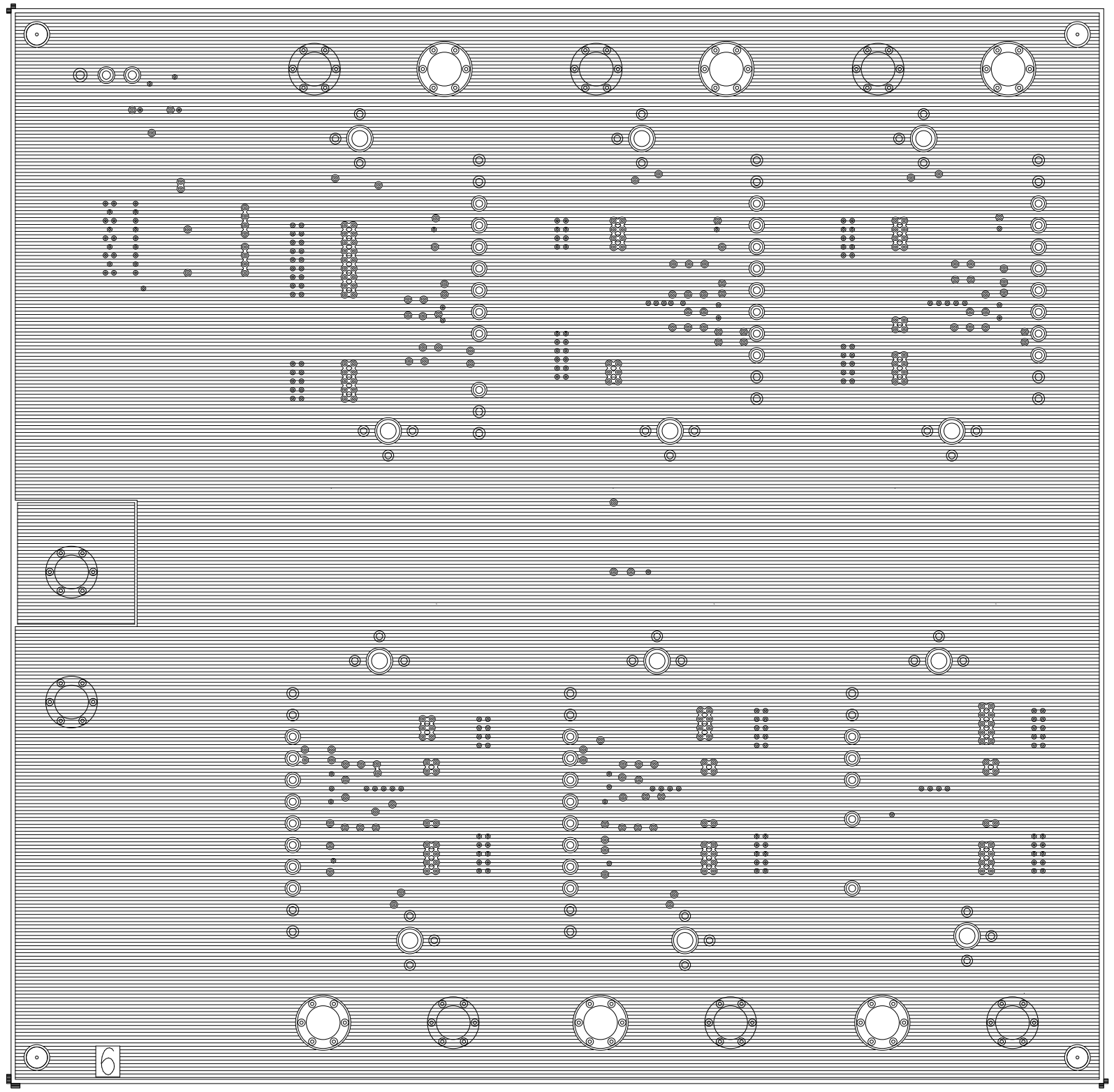
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE/ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P	ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 3 Foil [Top View]	PAGE COUNT: 12 OF 27	TYPE: PWB	SBU: AM1



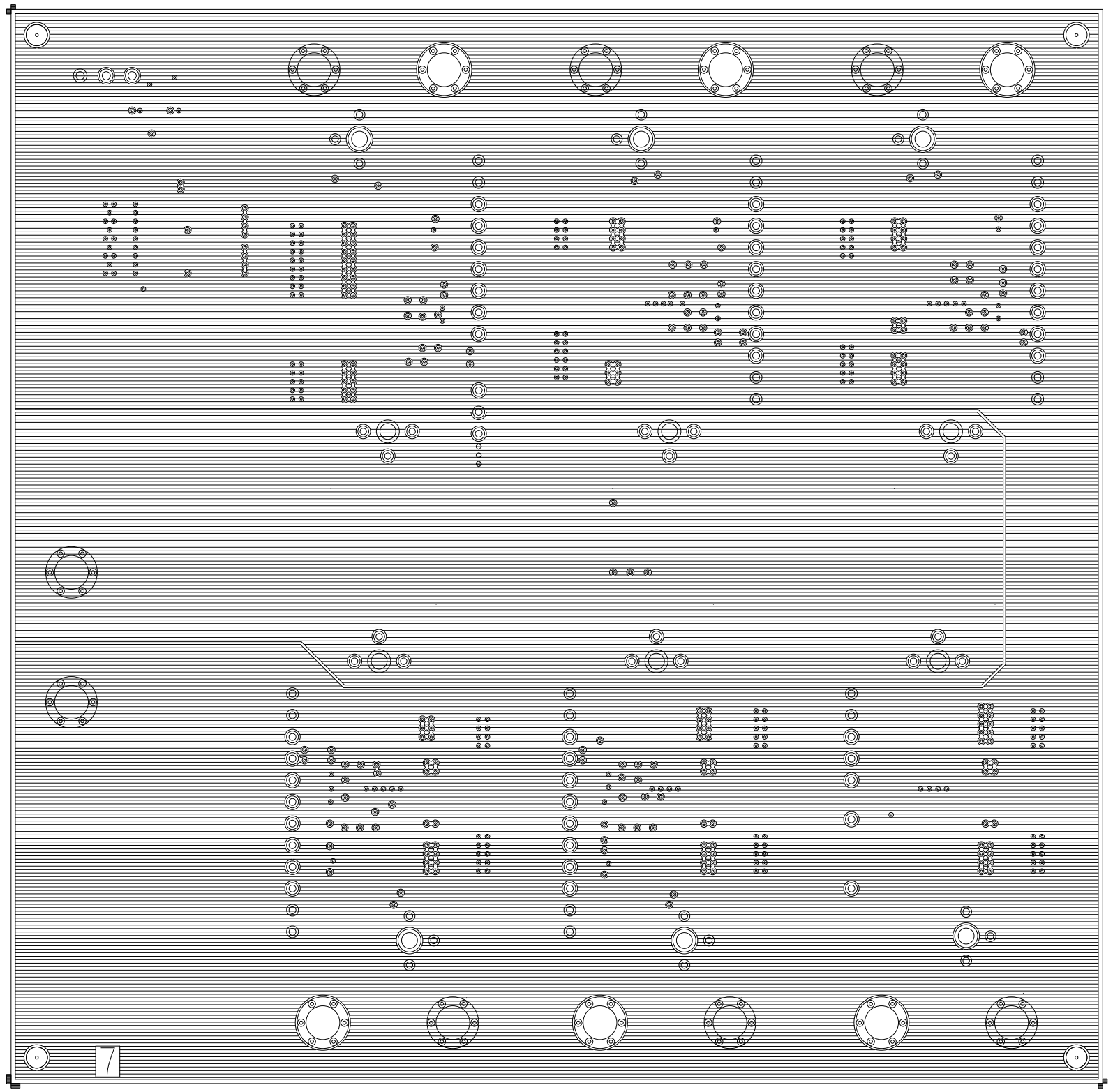
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE /ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 4 Foil [Top View]	PAGE COUNT: 13 OF 27	TYPE: PWB	SBU: AM1



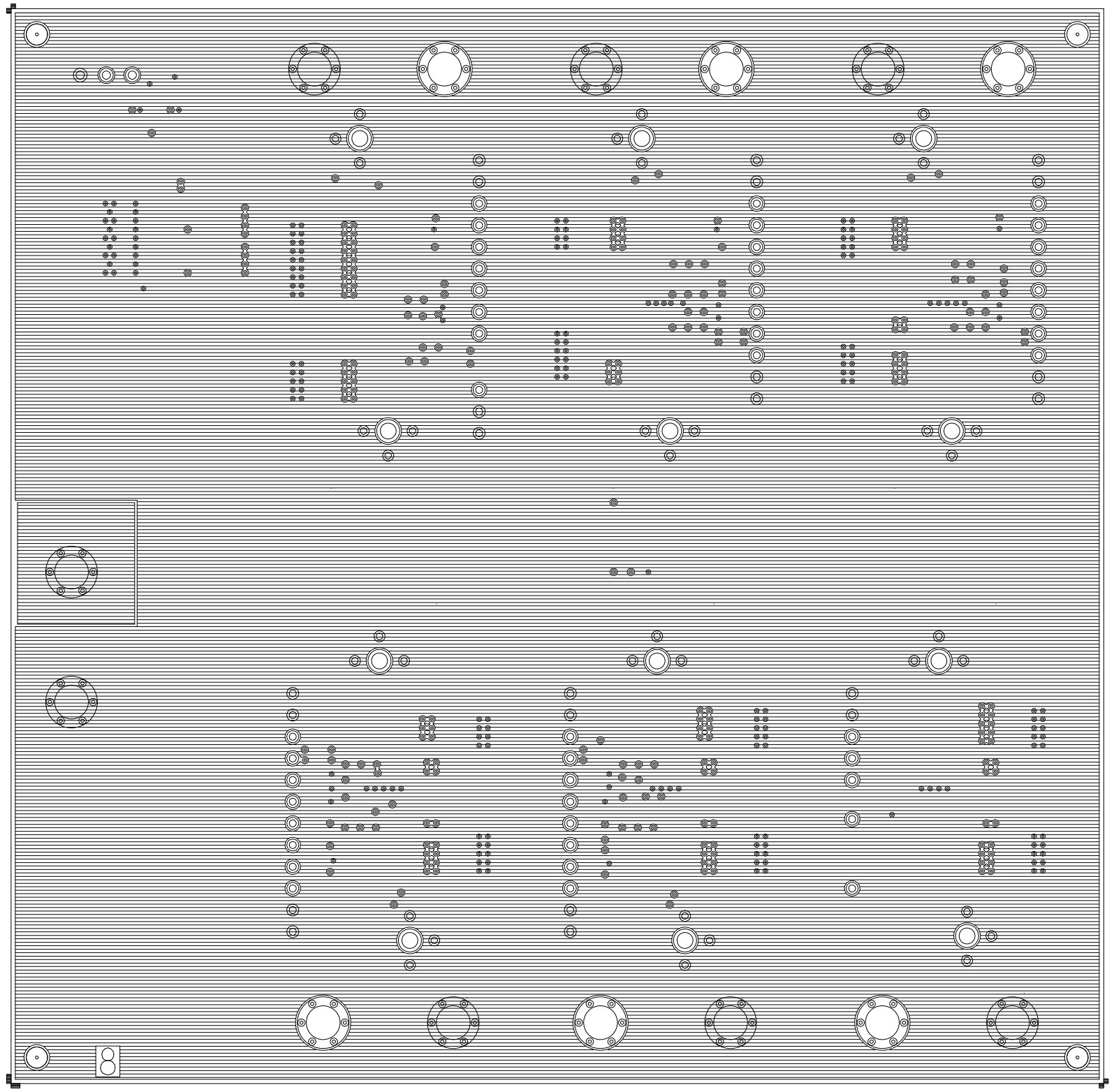
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE/ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 5 Foil [Top View]	PAGE COUNT: 14 OF 27	TYPE: PWB	SBU: AM1



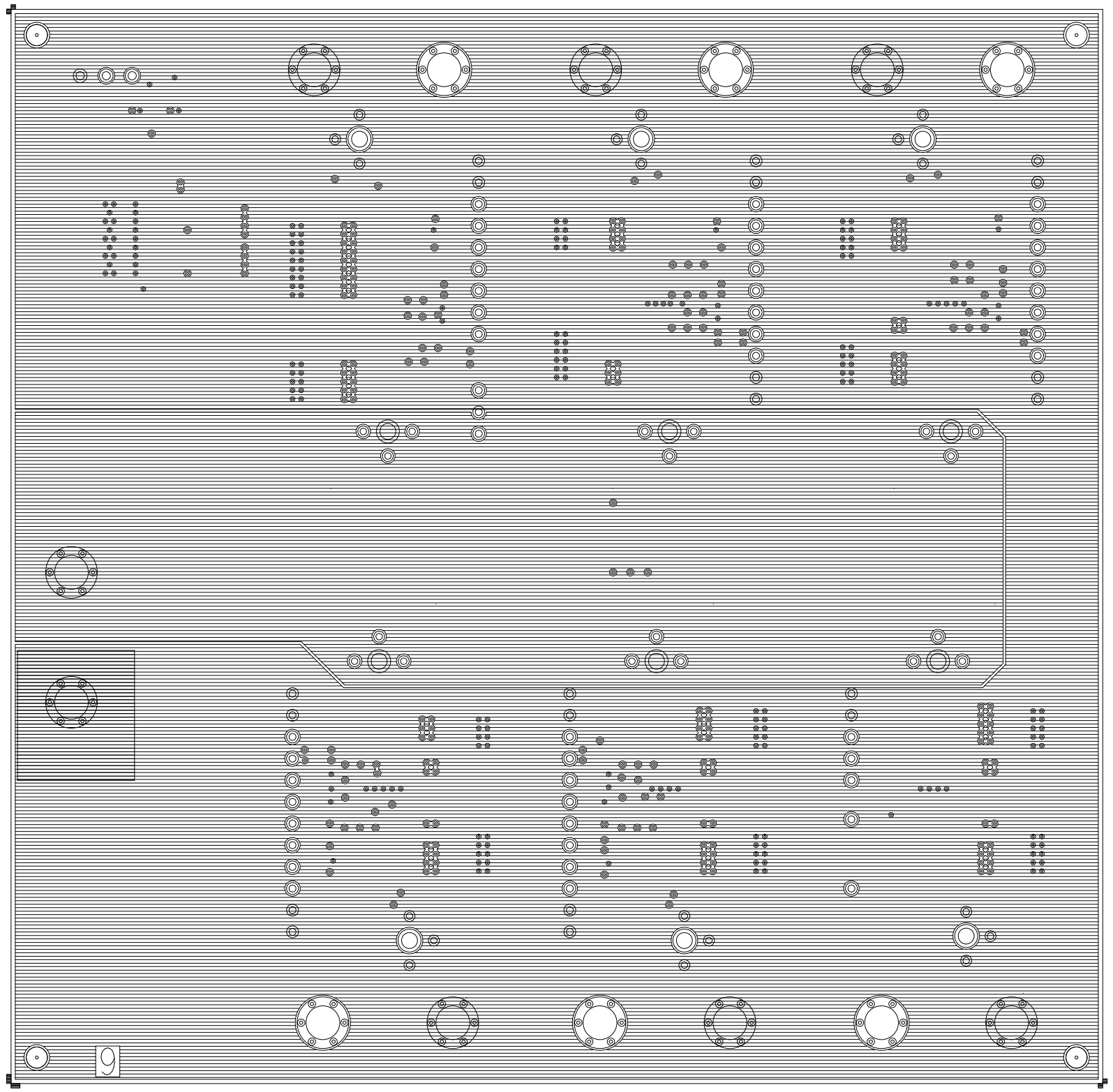
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE /ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 6 Foil [Top View]	PAGE COUNT: 15 OF 27	TYPE: PWB	SBU: AM1



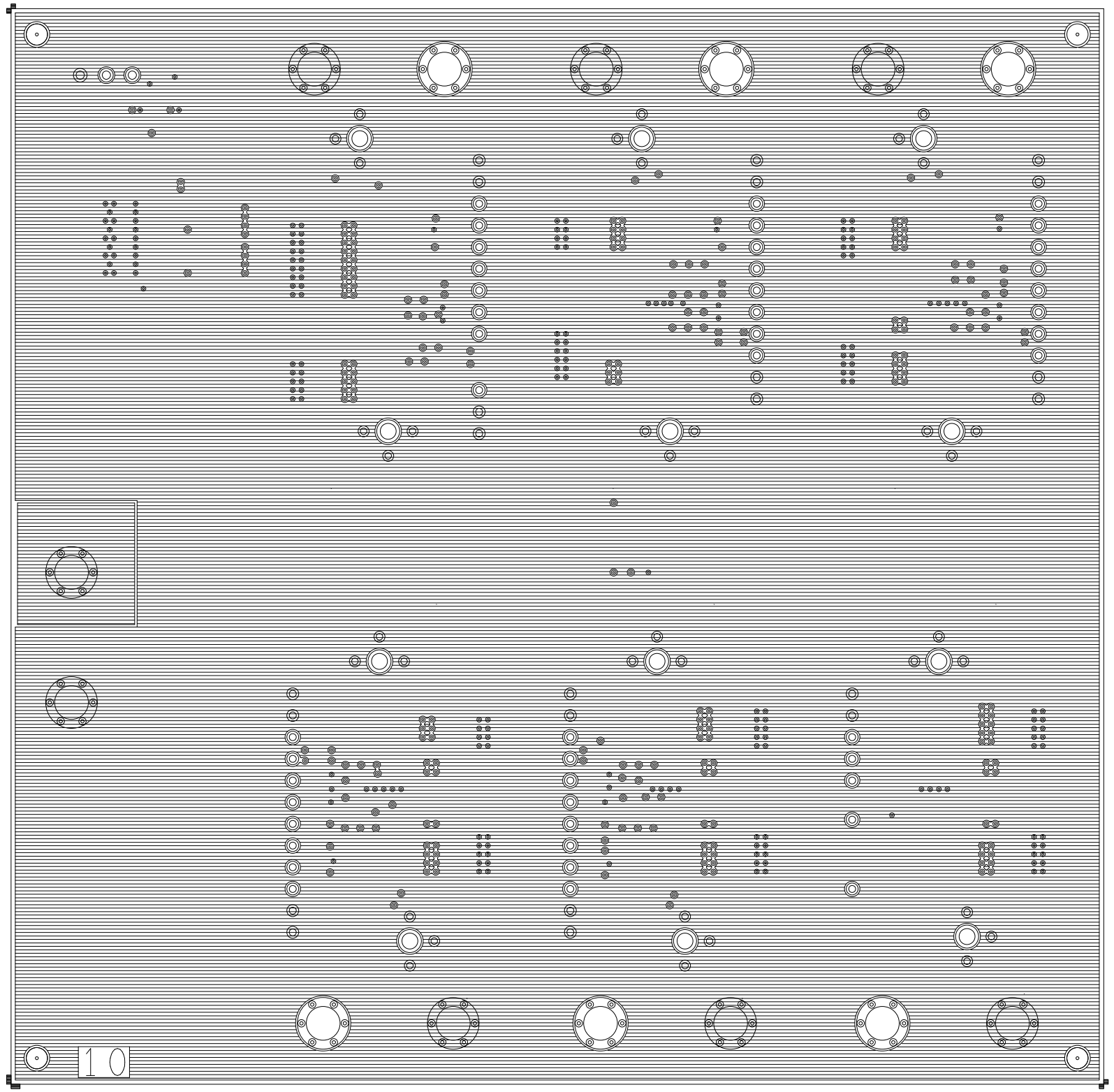
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 7 Foil [Top View]	PAGE COUNT: 16 OF 27	TYPE: PWB	SBU: AM1



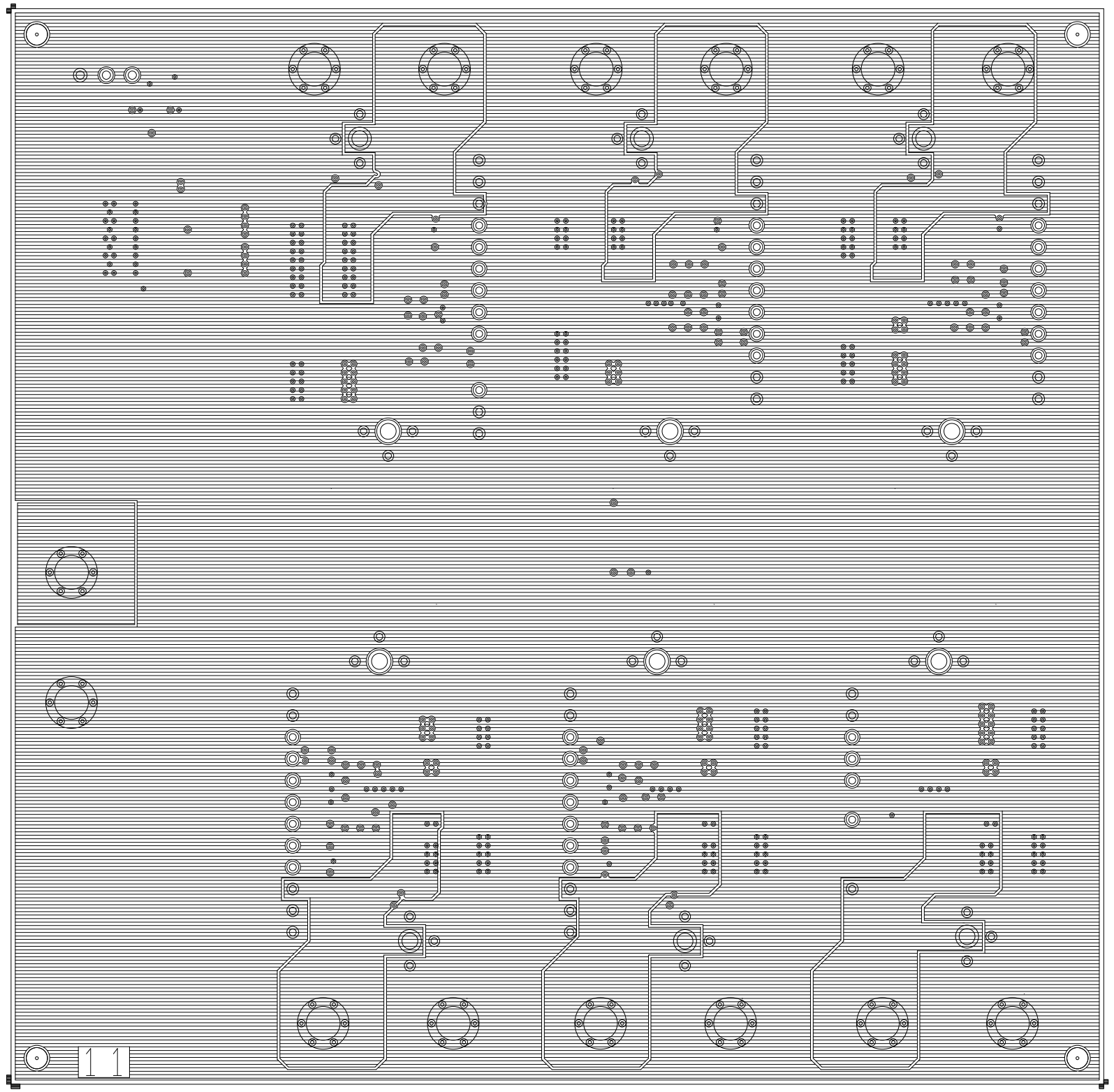
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 8 Foil [Top View]	PAGE COUNT: 17 OF 27	TYPE: PWB	SBU: AM1



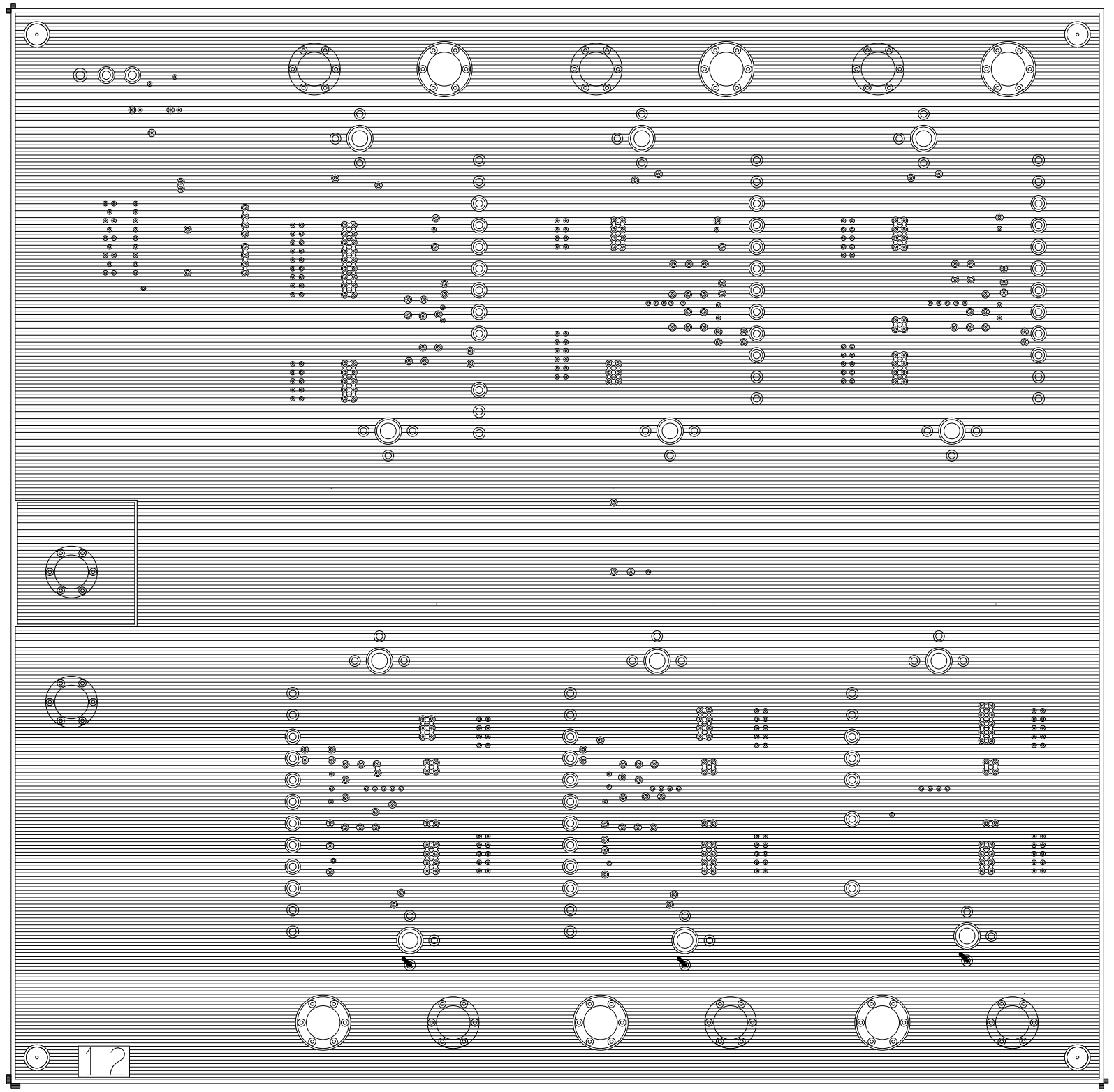
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 9 Foil [Top View]	PAGE COUNT: 18 OF 27	TYPE: PWB	SBU: AM1



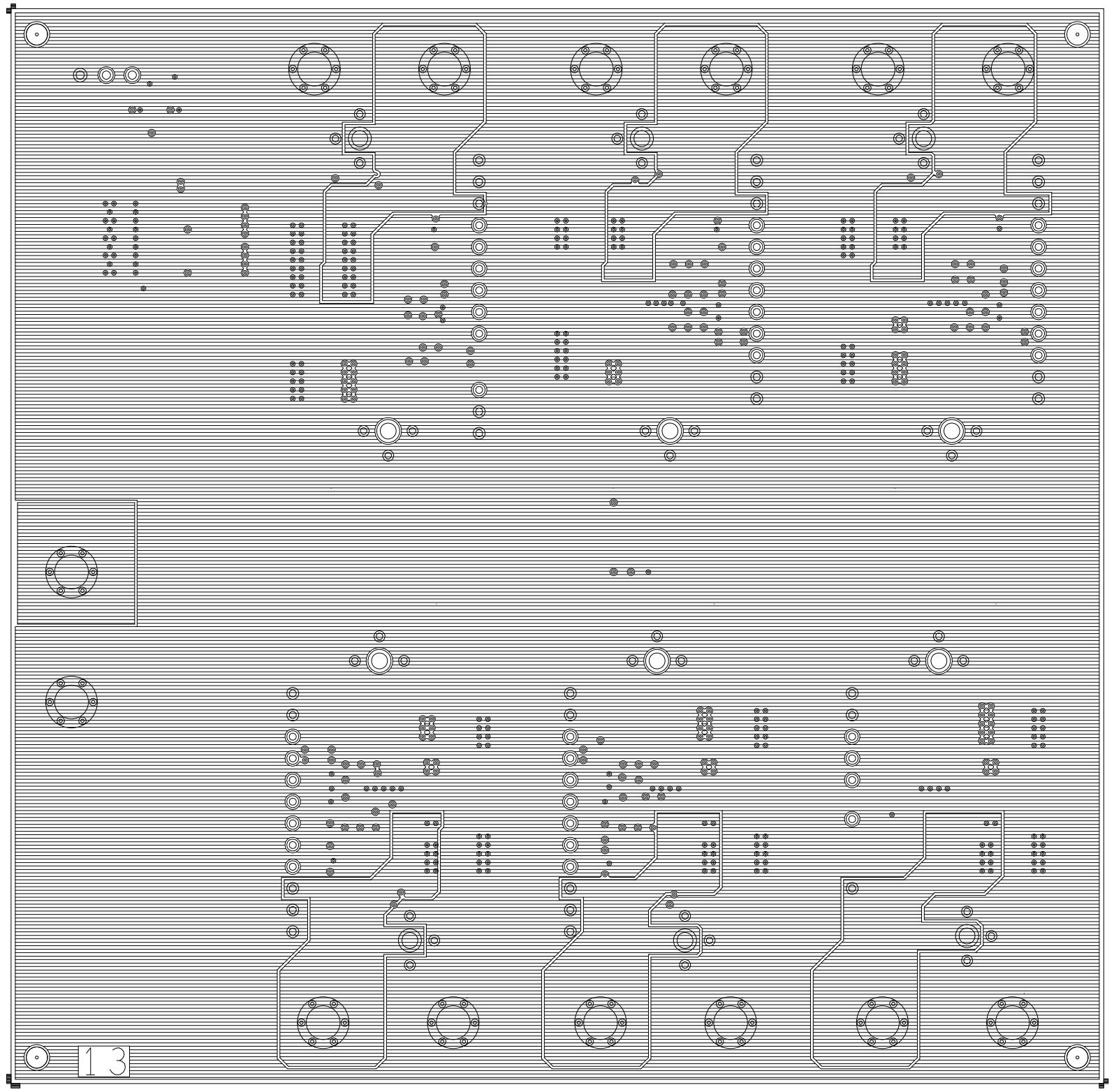
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 10 Foil [Top View]	PAGE COUNT: 19 OF 27	TYPE: PWB	SBU: AM1



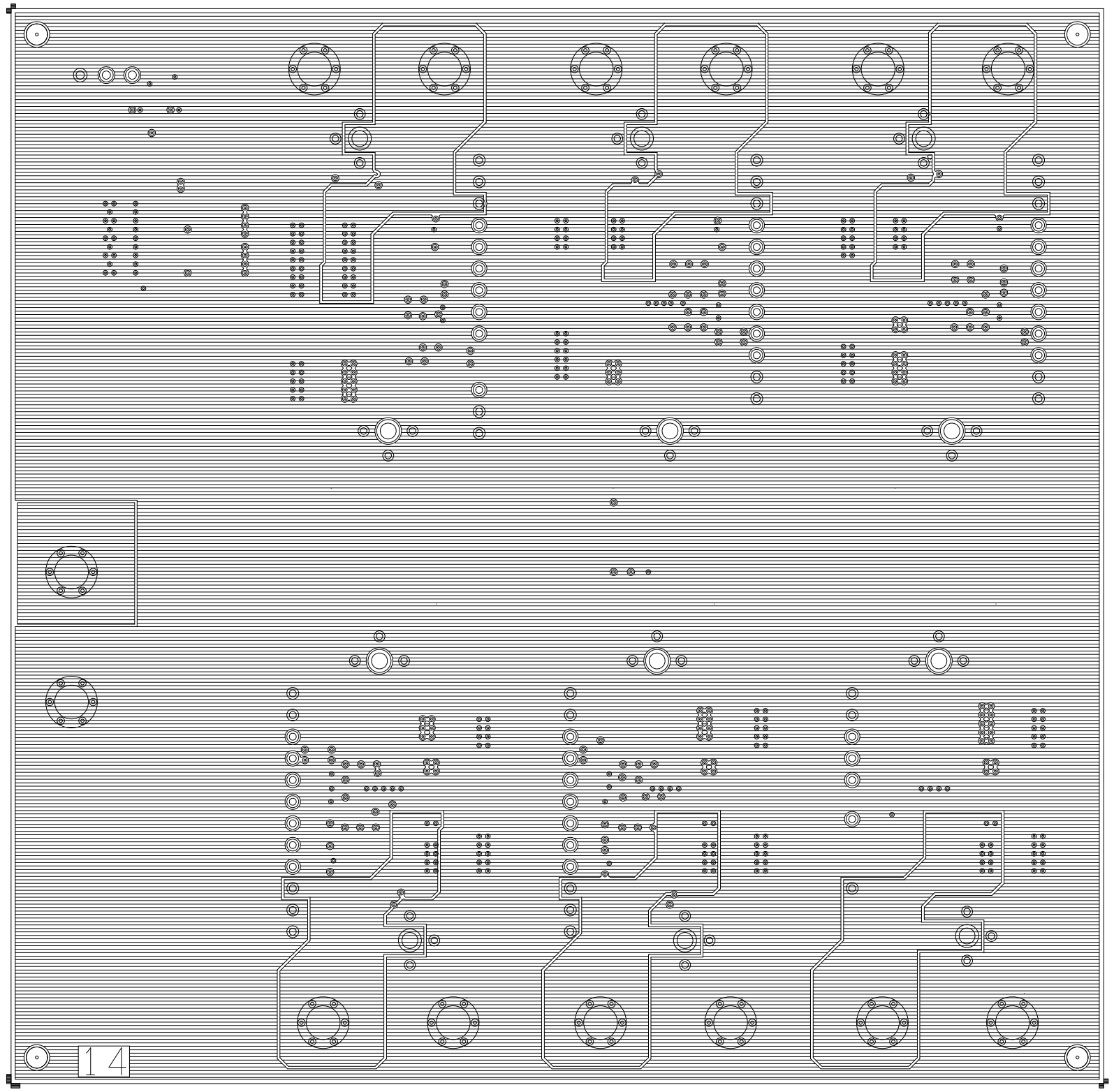
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 11 Foil [Top View]	PAGE COUNT: 20 OF 27	TYPE: PWB	SBU: AM1



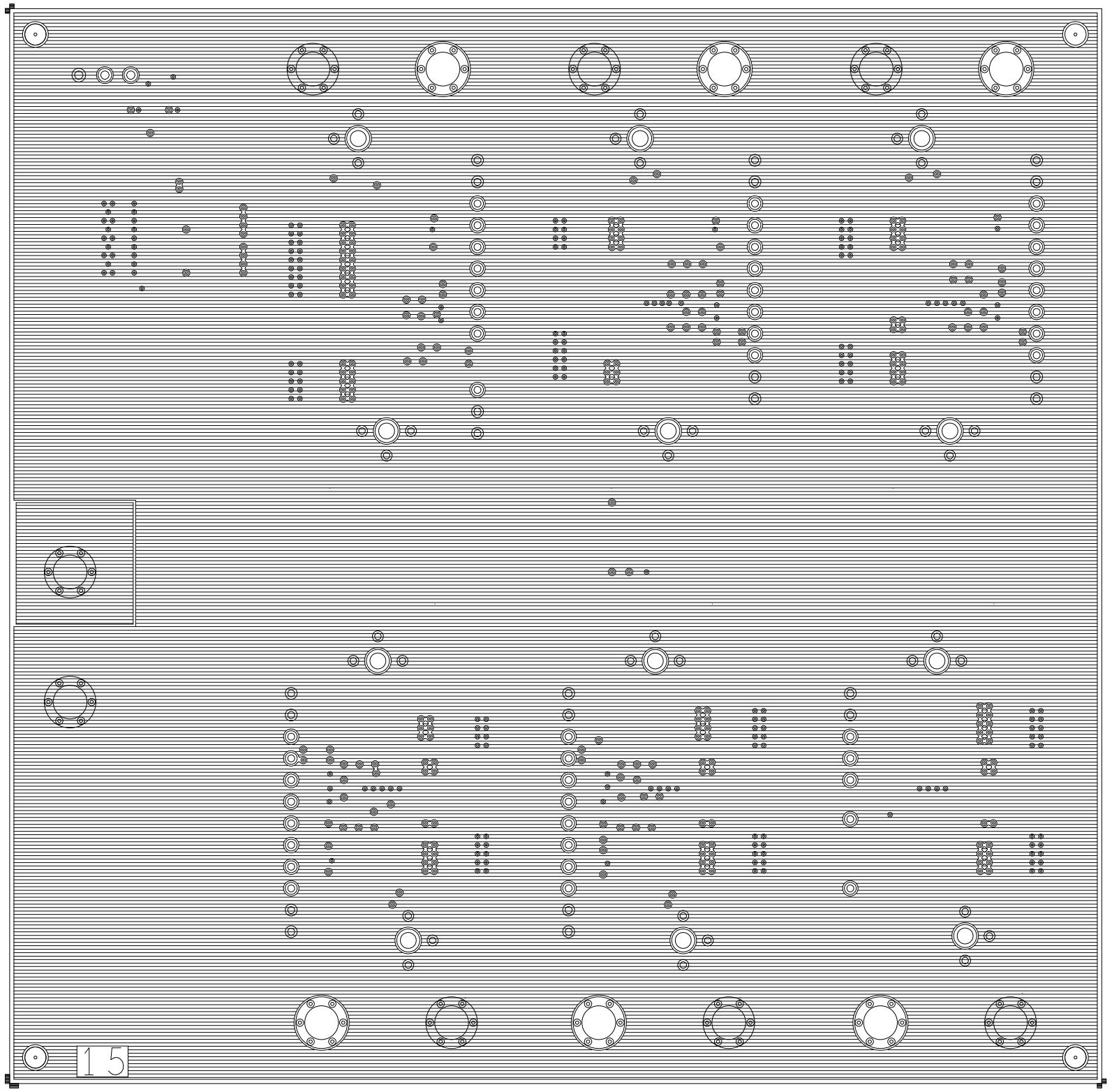
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 12 Foil [Top View]	PAGE COUNT: 21 OF 27	TYPE: PWB	SBU: AM1



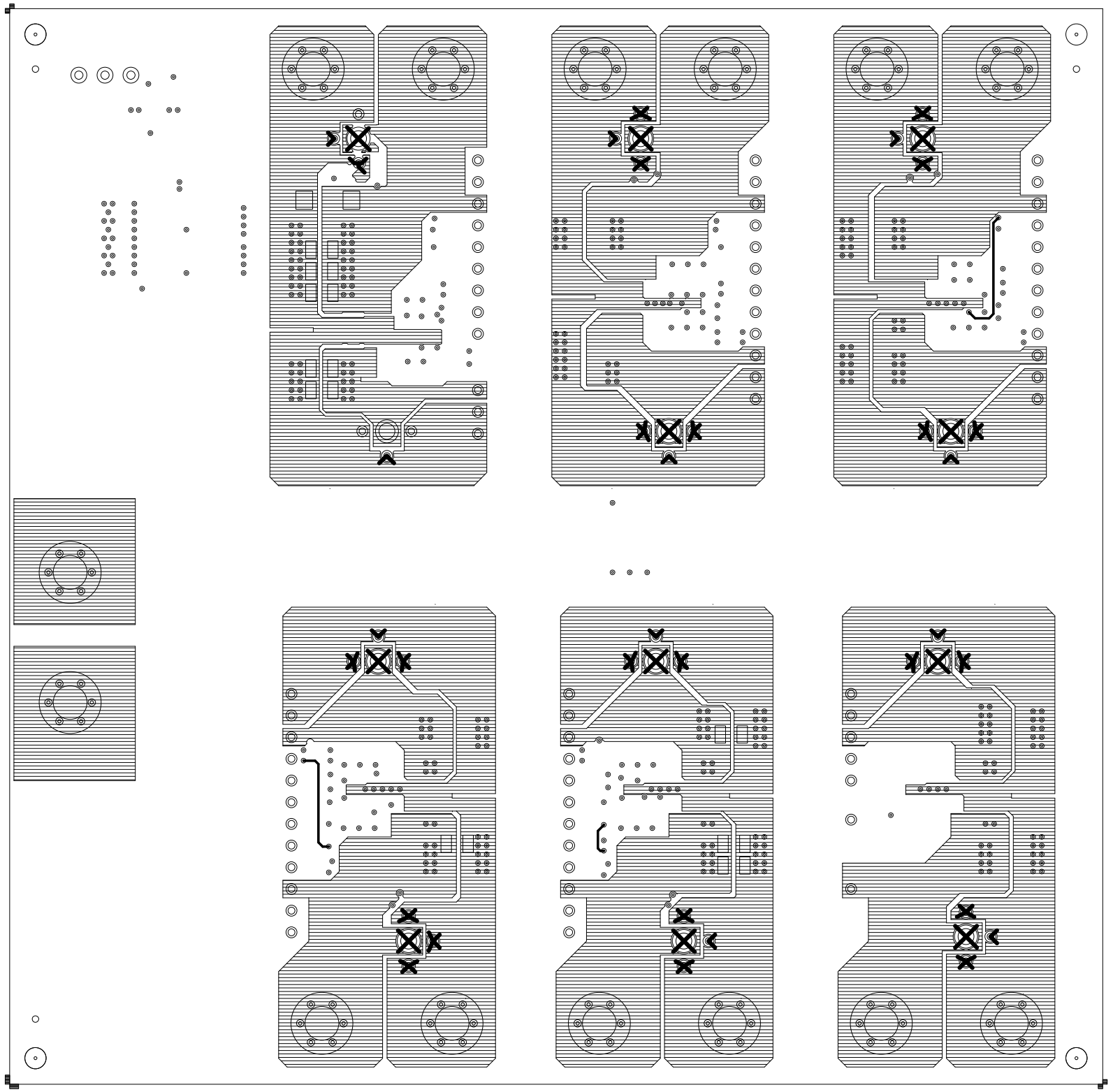
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				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 13 Foil [Top View]	PAGE COUNT: 22 OF 27	TYPE: PWB	SBU: AM1



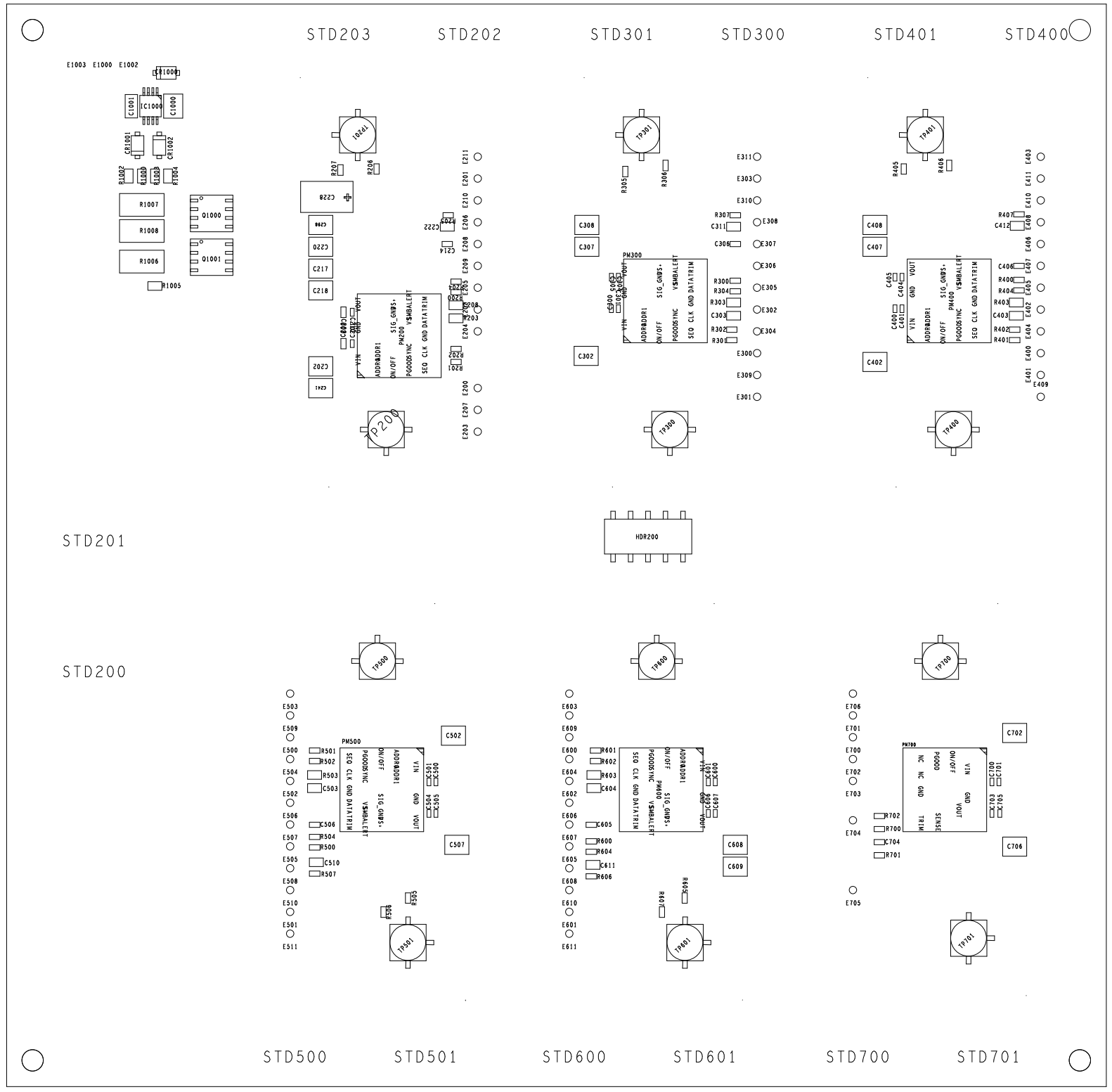
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE/ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 14 Foil [Top View]	PAGE COUNT: 23 OF 27	TYPE: PWB	SBU: AM1



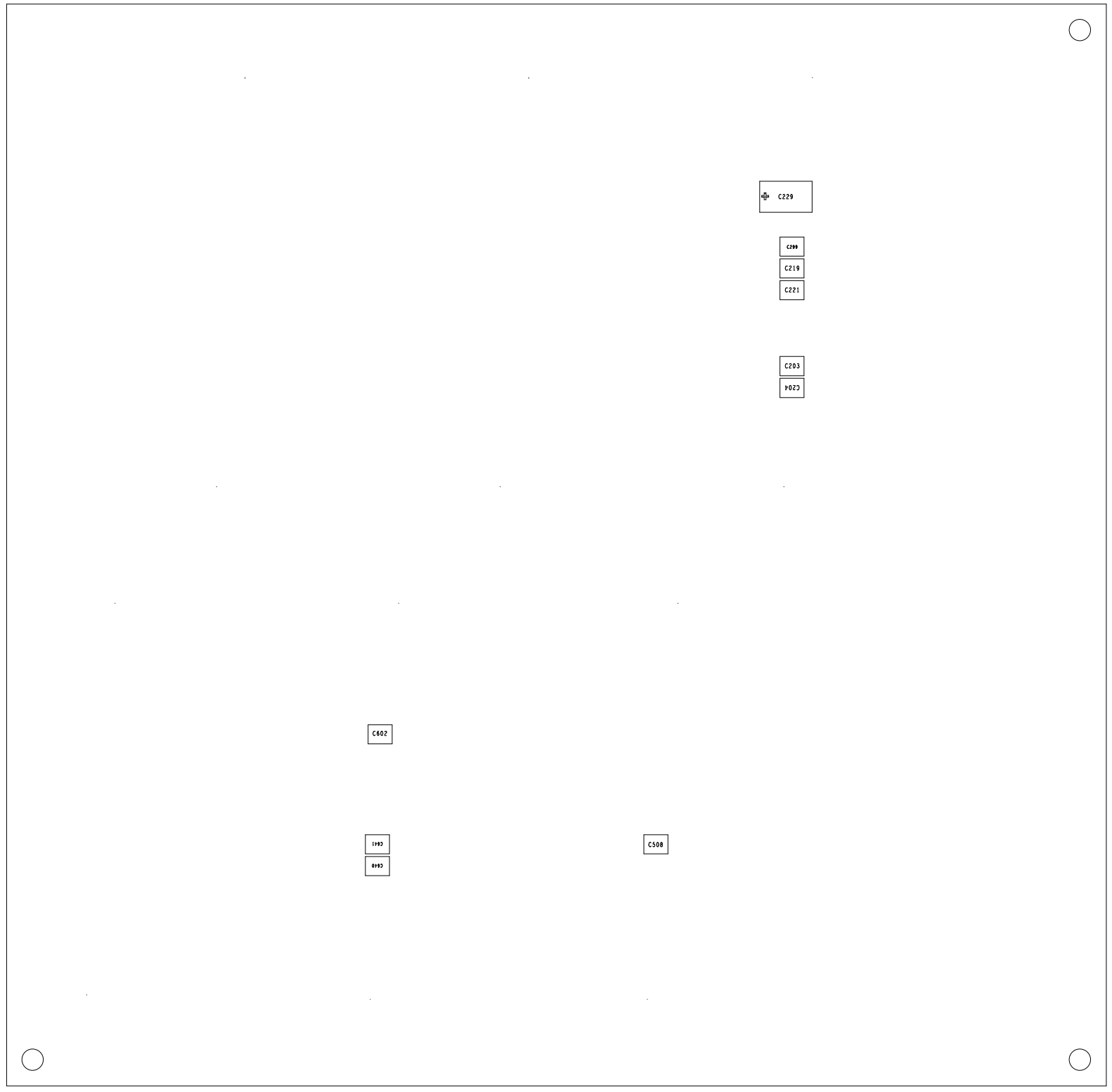
Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE /ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW PRINTED WIRING BOARD Layer 15 Foil [Top View]	PAGE COUNT: 24 OF 27	TYPE: PWB	SBU: AM1



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				DESCRIPTION/VIEW PRINTED WIRING BOARD Bottom Etch [Top View]	PAGE COUNT: 25 OF 27	TYPE: PWB	SBU: AM1



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				DESCRIPTION/VIEW BOARD ASSEMBLY Top Assembly [Top View]	PAGE COUNT: 26 OF 27	TYPE: PWB	SBU: AM1



Power Systems Proprietary Use Pursuant to Company Instructions	NAME: xilinx_xcku5p_1 COMCODE: 8600300853P PWB PROJECT: xilinx_xcku5p_1 UNITS: "mils" CKT X-DIM: 6300.0 CKT Y-DIM: 6200.0 LAYERS: 16	TOTAL SMT Pins: 386 TOTAL TH Pins: 136 TOTAL Drill Holes: 658 Overal Nom PWB Thickness: 101.8 Panel: no panel Previous PWB Comcode: INITIAL RELEASE Product Category: adw_projects	Date: 110819 User: 204063196	TITLE/ID 8600300853P PRINTED WIRING BOARD AM1 8600300853P-PWB	COMCODE: 8600300853P		ISSUE: 1
				DESCRIPTION/VIEW BOARD ASSEMBLY Bottom Assembly [Bottom View]	PAGE COUNT: 27 OF 27	TYPE: PWB	SBU: AM1